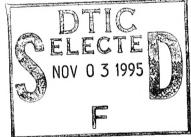


Multiplexers for Multifunction Electromagnetic Radiating Systems (MERS)

W. Henry T. Q. Ho M. Mills D. Rubin

Technical Document 2852 September 1995



Naval Command, Control and Ocean Surveillance Center RDT&E Division

> San Diego, CA 92152-5001

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# NAVAL COMMAND, CONTROL AND OCEAN SURVEILLANCE CENTER RDT&E DIVISION

San Diego, California 92152-5001

K. E. EVANS, CAPT, USN Commanding Officer

R. T. SHEARER Executive Director

#### **ADMINISTRATIVE INFORMATION**

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Released by L. C. Russell, Head Applied Electromagnetics Branch Under authority of R. J. Kochanski, Head Communications Systems Engineering and Integration Division

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### 1. INTRODUCTION

The Multiplexers for Multifunction Electromagnetic Radiating Systems (MERS) objective was to merge four different radio frequency (RF) sensor systems into a single-antenna structure. Currently, the four candidates are Combat Direction Finding (CDF), Identification Friend or Foe (IFF), Joint Tactical Information Distribution System (JTIDS), and Ultra-High-Frequency (UHF) communications. This report demonstrates that the merged antenna system is a low-cost system that decreases topside space and weight requirements while it also improves the multiple system performance. The ultimate goal is to provide a family of MERS antennas that meet each ship class antenna requirement in a combined and optimized structure.

MERS requires appropriate isolation between different systems to function properly. Multiplexing networks is one isolation method. Generally, a multiplexer can divide a frequency band into two separate bands or frequency packets. MERS requires several multiplexers to separate the Very-High-Frequency (VHF) portion of the CDF from the UHF, UHF communications from JTIDS, and IFF transmission from receiving signals. This report focuses on these multiplexers. The designs are preliminary. This report discusses the requirements, system concept for MERS, theoretical aspects, and the numerical results behind the multiplexer designs. Recommendations for future work are also included.

# 2. MULTIPLEXER ARCHITECTURE

#### 2.1 SYSTEM CONCEPT

Figure 1 shows the proposed MERS architecture. The MERS concept mirrors the struggle to eliminate "stove pipe" systems and replace them with shared assets. In this report, the shared assets are antennas, RF amplifiers, control and RF distribution systems. There are three principal elements to the MERS concept:

- 1. Circular array antenna
- 2. Wideband UHF communications architecture
- 3. Fiber-optic control and RF distribution system

The circular arrays antenna will consist of three low-profile antenna arrays that encircle the mast one above the other. The systems are combined such that JTIDS and UHF communications operate on the upper array, the Combat Direction Finder (CDF) operates on the middle array, and the IFF operates on the lower array. Three circular sets allow concurrent operation of all four systems and yet provide adequate isolation for electromagnetic interference (EMI) control. These arrays provide simultaneous transmit and receive capability, omnidirectional coverage, and directional coverage when required.

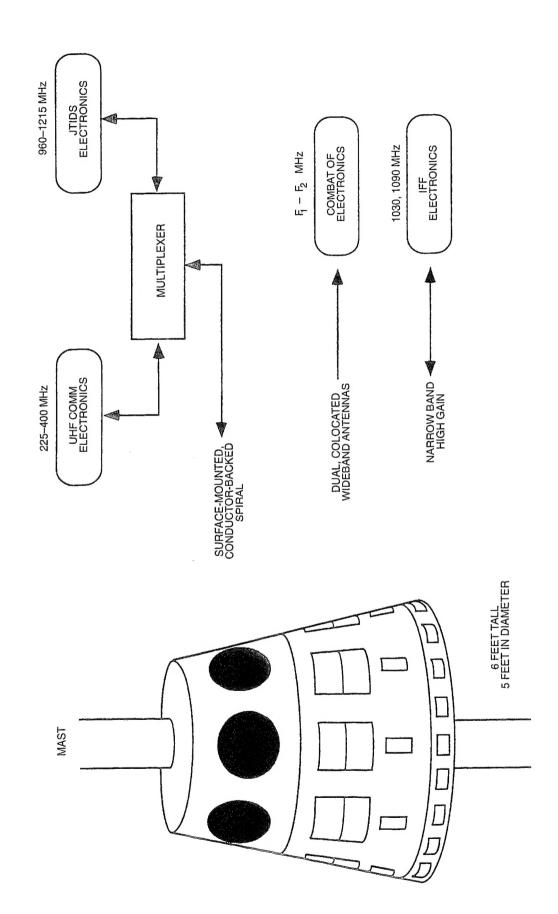


Figure 1. MERS system architecture.

#### 2.2 MULTIPLEXER REQUIREMENTS

As discussed previously, multiplexers will be used to provide isolation between different systems. The preliminary requirements for these multiplexers are described as follows:

The CDF Multiplexer separates the VHF portion of the CDF from the UHF portion. Table 1 describes the requirements for this multiplexer.

Characteristics	Capability Requirement
VHF Band	F <sub>1</sub> - F <sub>2</sub>
UHF Band	F <sub>3</sub> - F <sub>4</sub>
Insertion Loss	0.5 dB
Isolation	≥ 80 dB
VSWR	1.5:1
Power Rating	1 kW

Table 1. CDF multiplexer requirements.

The UHF/JTIDS Multiplexer separates UHF communications from JTIDS. Table 2 describes the requirements for this multiplexer.

Characteristics	Capability Requirement
UHF Band	225-400 MHz
JTIDS Band	960-1215 MHz
Insertion Loss	0.5 dB
Isolation	≥ 80 dB
voltage standing-wave ratio (VSWR)	1.4:1
Power Rating	10 kW

Table 2. UHF/JTIDS multiplexer requirements.

The  $T_X/R_X$  IFF Multiplexer separates the IFF reception from transmission. Table 3 describes the requirements for this multiplexer.

Characteristics	Capability Requirement
Transmission Band	1030 MHz
Reception Band	1090 MHz
Insertion Loss	0.5 dB
Isolation	≥ 80 dB
VSWR	1.4:1
Power Rating	10 kW

Table 3. T<sub>x</sub>R<sub>x</sub> multiplexer requirements.

#### 2.3 DESIGN CONSIDERATIONS

The first two multiplexers are designed with lumped elements, while the last multiplexer uses coaxial line resonators. These multiplexers use Chebyshev designs. Ladder networks can be either a  $\pi$  or T configuration. Figure 2 shows the  $\pi$  configuration type. Figure 3 shows the T configuration type.

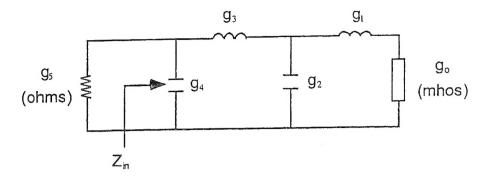


Figure 2. Ladder network for  $\pi$  configuration.

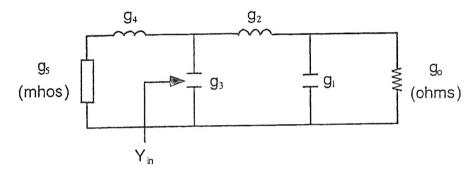


Figure 3. Ladder network for T configuration.

The circuit parameters can be computed as follows (Matthaei, Young & Jones, 1964):

$$X = \frac{1}{2N} \cdot \ln \left[ \coth \left( \frac{R}{17.37} \right) \right] \tag{1}$$

$$a_k = \sin\left[(2K - 1)\frac{\pi}{2N}\right] \quad K = 1, 2, 3, ...N$$
 (2)

$$b_k = \sinh^2(x) + \sin^2\left(\frac{K\pi}{N}\right) \tag{3}$$

$$g_1 = \frac{2a_1}{\sinh(x)} \tag{4}$$

for K=2 to N 
$$g_k = 4 \left[ \frac{a_{K-1} \cdot a_K}{b_{K-1} \cdot g_{K-1}} \right]$$
 (5)

for N odd ... 
$$g_{N+1} = 1$$
 (6)

for N even ... 
$$g_{N+1} = \coth^2\left(\frac{Nx}{2}\right)$$
 (7)

where R = ripple (dB),  $g_0 = 1$ , and N = the number of reactive elements. These two ladder networks are "duals" of each other. This is because the equations for the input impedance (first element capacitor) and conductance (first element inductor) give identical reflection coefficients and therefore, the same transmission for purely reactive circuits.

#### 2.3.1 Low-Pass and High-Pass Filters

Designing a low-pass filter simply involves changing the g values to conform with the actual impedance of the source. For a high-pass filter, we may use 1/g values and change series inductors to

capacitors and parallel capacitors to inductors. Figure 4 shows the configuration for a low-pass filter and figure 5 shows the configuration for a high-pass filter.

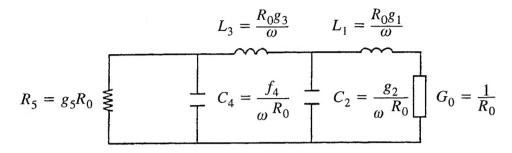


Figure 4. Configuration for low-pass filter.

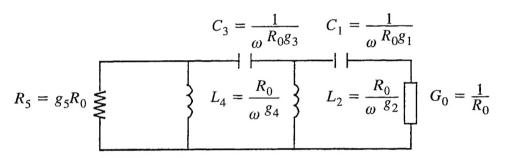


Figure 5. Configuration for high-pass filter.

#### 2.3.2 Band-Pass Filters

For a band-pass filter, NRaD researchers used the following technique by first changing the prototype from series inductors to series latch-checking (LC) circuits that resonate at  $\omega_0$ . The values were computed as follows:

$$L_{K} = \frac{g_{K}R_{0}}{\omega_{0}W}, C_{K} = \frac{1}{\omega_{0}^{2}L_{K}}, W = \frac{BW}{f_{0}},$$
 (8)

with BW representing the filter's bandwidth. Next the researchers changed the prototype parallel capacitances to parallel LC circuits that resonate at  $\omega_0$ ,

$$C_{J} = \frac{g_{J}}{\omega_{0}WR_{0}} \quad \text{and} \quad L_{J} = \frac{1}{\omega_{0}^{2}C_{J}}.$$
 (9)

Appendix A provides proof of this method.

## 2.3.3 High-Pass and Low-Pass Multiplexers

If low-pass and high-pass filters are used to design a multiplexer, the input impedance must be directly matched into the source. The impedance presented to the source will be the parallel combination of the two filters. At each frequency, the current through one filter is dependent on the impedance presented by the other. One may design high-pass and low-pass multiplexers using g values for low-pass prototypes with a source impedance of zero ohms. In this case, the current through each filter is independent of the other in order to maintain the Chebyshev or Butterworth characteristics. If source impedance equal to the load impedance is now used, the voltage at the junction will be

approximately half the source voltage. This results in the equal-ripple or maximally flat responses maintained for each filter.

A multiplexer formed by high-pass and low-pass maximally flat filters will yield a 3-dB crossover with excellent VSWR throughout the designated bandwidth. In practice, filters are not lossless, therefore, the crossover attenuation will be slightly higher than 3 dB. In the case of Chebyshev filters, the cutoff slopes are much steeper for the same number of elements, and therefore, these configurations are common. The corresponding equations for Chebyshev singly connected prototypes are given in Matthaei et al. (1964).

$$\beta = \ln \left[ \coth \frac{R}{17.37} \right], \quad \gamma = \sinh \left( \frac{\beta}{2N} \right)$$
 (10)

$$a_K = \sin\left[\frac{\pi(2K-1)}{2N}\right], \quad K = 1, 2, ...N$$
 (11)

$$d_{K} = \left[ Y^{2} + \sin^{2}\left(\frac{\pi K}{2N}\right) \right] \cos^{2}\left(\frac{\pi K}{2N}\right), \quad K = 1, 2, ...N - 1$$
 (12)

$$g_1 = \frac{a_1}{r}, \quad g_{N+1} = \infty$$
 (13)

$$g_K = \frac{a_K a_{K-1}}{d_{K-1} g_{K-1}}, \quad K = 1, 2, ...N$$
 (14)

# 2.3.4 Filter Design Using Parallel Resonators

Most microwave circuits use either series or parallel resonators but not both. A short-circuited coaxial cable line acts like a parallel LC circuit near its quarter wavelength center frequency. Excellent filters with bandwidths less than 20 percent can be obtained using these resonators coupled with either inductors or capacitors. The two popular inverters use  $\pi$  connections of either inductors or capacitors. These elements behave as ideal inverters, especially near the center frequency, as can easily be shown using ABCD matrices. Appendix B describes the steps necessary to go from series parallel LC band-pass filters to capacitive coupled quarter-wave shorted lines (Sagawa, Makimoto, & Yamashita, 1985).

# 3. NUMERICAL RESULTS

This section presents circuit diagram and performance data for the multiplexers. Figure 6 shows a schematic of the UHF/JTIDS multiplexer. As mentioned before, the UHF/JTIDS multiplexer separates the UHF communications from JTIDS signals. The UHF band is from 225 to 400 MHz, and the JTIDS band is from 960 to 1215 MHz. The design consists of one high-pass filter, one low-pass filter, and two band-pass filters. The low-pass filter passes all frequencies below 620 MHz. These frequencies are then passed through a band-pass filter that coincides with the UHF bandwidth. The high-pass filter passes frequencies greater than 620 MHz. These frequencies are then passed through a band-pass filter coinciding with the JTIDS bandwidth. Figure 7 shows the numerical data of the UHF/JTIDS multiplexer. The plot shows the insertion loss as a function of frequency. Insertion loss is the loss of power in a load that occurs when a network is inserted between the load and generator supplying the load. The numerical data show the design has about 0.2-dB insertion loss and more than 80-dB isolation between the two systems.

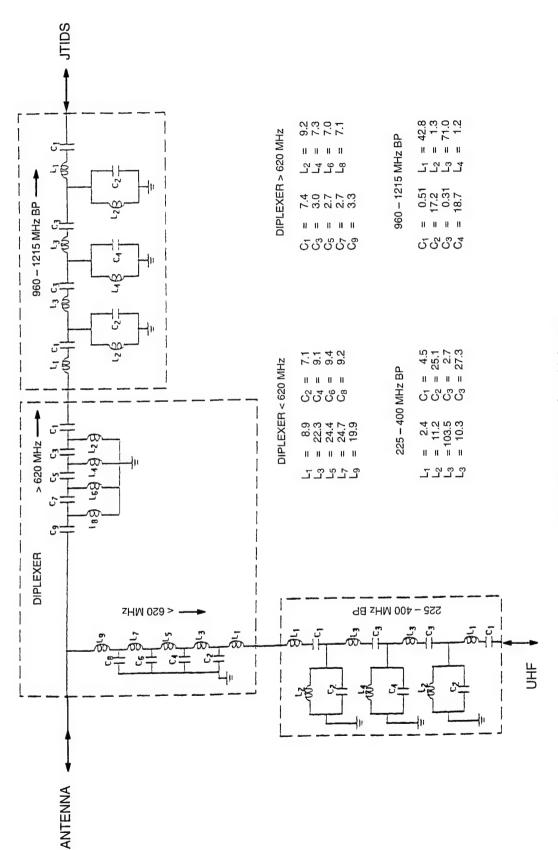


Figure 6. UHF/JTIDS multiplexer.

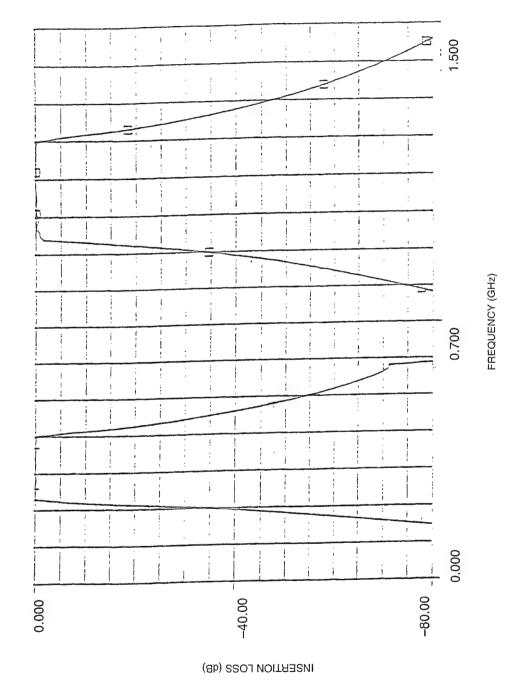
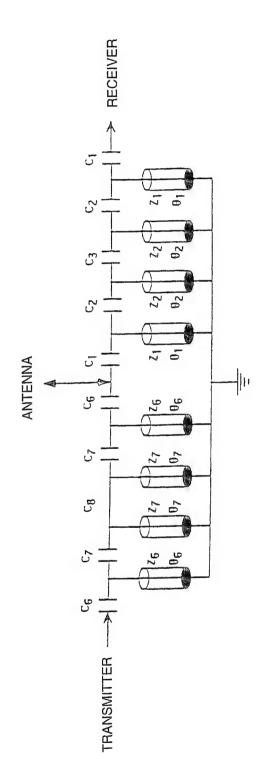


Figure 7. Performance for UHF/JTIDS multiplexer.

The IFF multiplexer has a much narrower bandwidth than the UHF/JTIDS multiplexer. In this case the multiplexer separates IFF transmission and receiving signals. The transmission bandwidth is from 1025 to 1035 MHz, and the bandwidth for receiving is from 1085 to 1095 MHz. Figure 8 shows the schematic of the IFF multiplexer while figure 9 shows the performance. As illustrated, the insertion loss is about 0.1 dB in the passbands, and isolation is more than 80 dB between the transmitter and the receiver. In this design, the passbands are 1025 to 1035 MHz and 1085 to 1095 MHz.

Figure 10 shows the proposed circuit diagram for the Combat DF multiplexer. The operating frequencies for the Combat DF are classified, therefore, they will not be discussed here. However, the design is made up of two high-pass filters, one low-pass filter, and one band-pass filter. Figure 11 shows the trend for the frequency response of this multiplexer.



1		-
47 = 89.2°	i i	$\theta_2 = 89.3^{\circ}$
C7'= .302	ر1ر9 = ٥٥ کد	C1 = .278
C <sub>B</sub> = .023		$C_2 = .021$
cg ≈ .018		$C_3 = .016$

Figure 8. IFF multiplexer schematic.

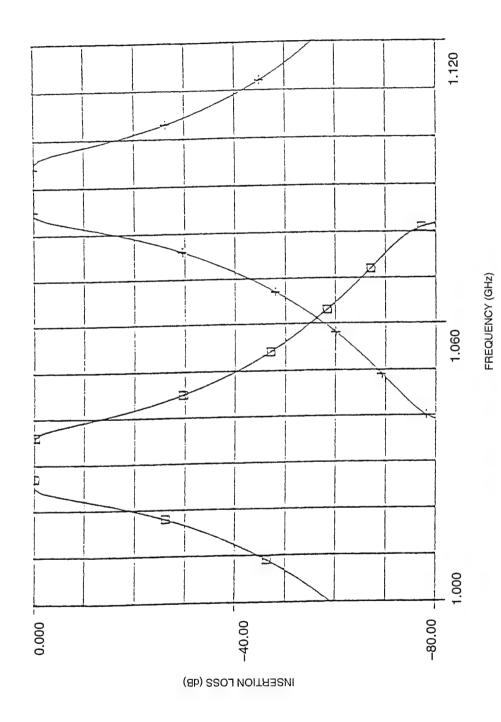


Figure 9. IFF multiplexer performance.

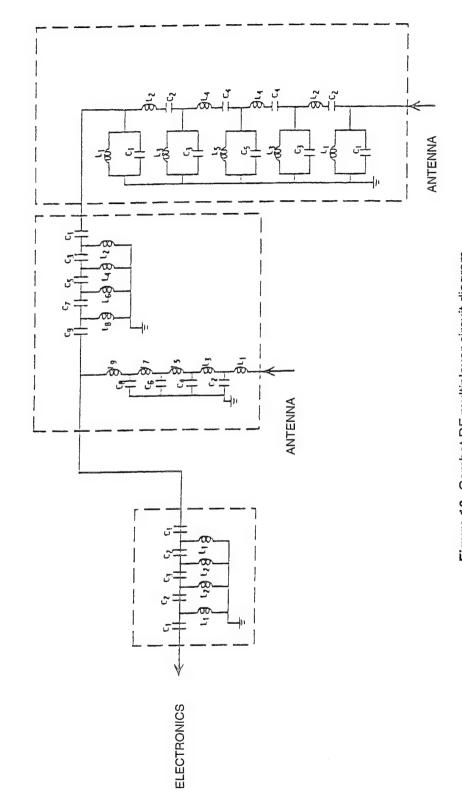


Figure 10. Combat DF multiplexer circuit diagram.

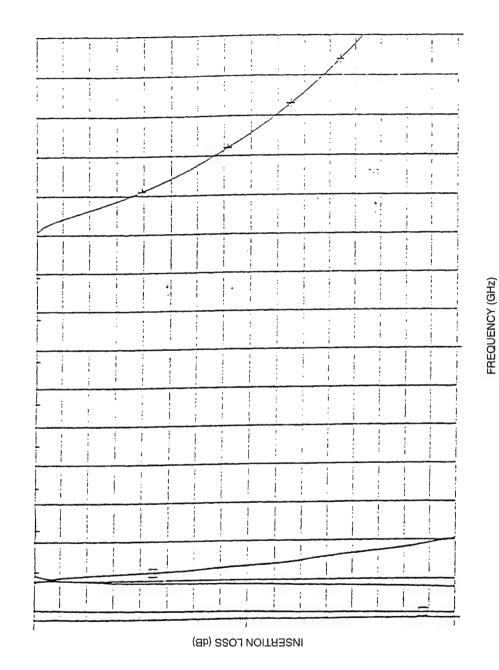


Figure 11. Combat DF multiplexer performance trend.

# 4. RECOMMENDATIONS AND CONCLUSIONS

There are many possible filter/multiplexer configurations that can be used to satisfy the requirements. Depending on allowable ripple and crossover frequencies, filters having different numbers of reactive elements might be preferable. Three multiplexers were designed. The multiplexers are used to separate VHF from UHF Combat DF, UHF communications from JTIDS, and the IFF transmission from reception. The designs still need to be standardized to commercially available component values. Prototypes need to be fabricated and tested.

Although the frequencies of these filters are relatively low for microwaves, line lengths of connections must be kept minimal to minimize effects from parasitic elements. This is particularly true for wire connections from capacitors to adjacent capacitors. In the case of the IFF multiplexer, it would be necessary to ensure that parasitic inductors will play a minimal role in determining the response of the multiplexer. Standard values for lumped elements must be specified before construction of the actual filter. The issues regarding power rating, size, and weight must also be addressed before the design is finalized.

# 5. REFERENCES

- 1. Matthaei, G. L., L. Young, and E. M. T. Jones. 1964. *Microwave Filters, Impedance Matching Networks, and Coupling Structures*, McGraw-Hill, New York, NY.
- 2. Sagawa, M., M. Makimoto, and S. Yamashita. 1985. "A Design Method of Band-pass Filters Using Dielectric-Filled Coaxial Resonators," *IEEE Transactions, Microwave Theory and Techniques* (Feb), pp. 152-157.

#### APPENDIX A

As discussed earlier, low-pass ladder networks consist of series inductors and parallel capacitors. Ladder circuits can be designed with series latch-checking (LC) and parallel LC resonant circuits. Consider figure A-1, which shows such a transformation.

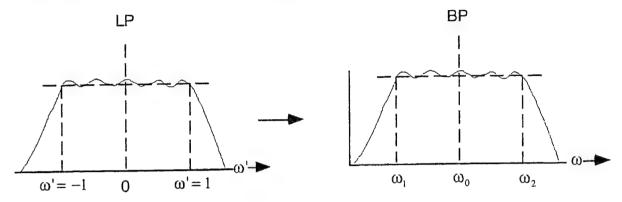


Figure A-1. Transformation from low-pass to band-pass.

Frequencies transform such that:

$$\omega' = \frac{\omega - \frac{\omega_1 \omega_2}{\omega}}{\omega_2 - \omega_1} \implies \omega = \omega_2 \text{ when } \omega' = 1$$

$$\omega = \omega_1 \text{ when } \omega' = -1$$

$$\omega = \sqrt{\omega_1 \omega_2} \equiv \omega_0 \text{ when } \omega' = 0$$
(A-1)

Let 
$$\Delta \omega = \omega_2 - \omega_1$$
, then  $\omega' = \frac{\omega^2 - \omega_1 \omega_2}{\omega(\omega_2 - \omega_1)}$  or  $\omega' = \frac{\omega^2 - \omega_0^2}{\omega \Delta \omega}$  (A-2)

We select one of the dual circuits shown in figure A-2, and scale it to match impedance  $R_0$ .

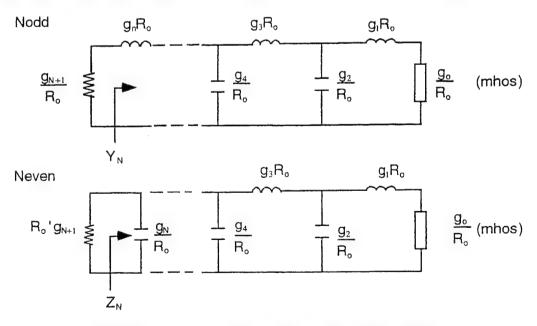


Figure A-2. Dual circuits for band-pass transformation.

To yield the following equations,

$$j\omega g_{N}^{'} + \frac{1}{j\omega g_{K}^{'} + \dots + \frac{1}{j\omega g_{K}^{'} + \dots + \frac{1}{g_{0}}}}$$

$$for K=1, \dots N$$

$$\begin{cases} g_{K} = g_{K}R_{0} \text{ (inductor)} : K \text{ odd} \\ g_{K} = \frac{g_{K}}{R_{0}} \text{ (capacitor)} : K \text{ even} \end{cases}$$

As a result, the LC equivalents are:

$$\begin{split} \text{K odd:} & = \mathrm{j} \bigg( \frac{\omega^2 - \omega_0^2}{\omega} \bigg) L_K = \mathrm{j} \bigg( \frac{\omega^2 - \omega_0^2}{\omega \Delta \omega} \bigg) L_k \Delta \omega = \mathrm{j} \bigg( \frac{\omega^2 - \omega_0^2}{\omega \Delta \omega} \bigg) L_K \omega_0 W \\ \text{K odd:} & \mathrm{j} \omega [\mathrm{g}_K'] \quad \Rightarrow \quad \mathrm{j} \bigg( \frac{\omega^2 - \omega_0^2}{\omega \Delta \omega} \bigg) L_K \omega_0 W \\ & \mathrm{g}_K' = \mathrm{g}_K R_0 = L_K \omega_0 W \quad \Rightarrow \quad L_K = \frac{\mathrm{g}_K R_0}{\omega_0 W} : \ C_K = \frac{1}{\omega_0^2 L_K} \\ \text{K even:} & \mathrm{j} Y = \mathrm{j} \bigg( \omega C_K - \frac{1}{\omega L_K} \bigg) = \mathrm{j} C_K \bigg( \omega - \frac{1}{\omega C_K L_K} \bigg) = \mathrm{j} C_K \bigg( \omega = \frac{\omega_0^2}{\omega} \bigg) \\ & = \mathrm{j} C_K \bigg( \frac{\omega^2 - \omega_0^2}{\omega} \bigg) = \mathrm{j} \bigg( \frac{\omega^2 - \omega_0^2}{\omega \Delta \omega} \bigg) C_K \Delta \omega = \mathrm{j} \bigg( \frac{\omega^2 - \omega_0^2}{\omega} \bigg) C_K \omega_0 W \\ \text{K even:} & \mathrm{j} \omega \mathrm{g}_K' \Rightarrow \mathrm{j} \bigg( \frac{\omega^2 - \omega_0^2}{\omega \Delta \omega} \bigg) [C_K \omega_0 W] \\ & \mathrm{g}_K' = \frac{\mathrm{g}_K}{R_0} = C_K \omega_0 W \qquad \Rightarrow \quad C_K = \frac{\mathrm{g}_K}{\omega_0 W R_0} : L_K = \frac{1}{\omega_0^2 C_K} \end{split}$$

#### APPENDIX B

Appendix B shows the necessary steps to go from series/parallel LC band-pass filters to capacitive coupled quarter-wave shorted lines. Referring to steps 1–5 of figure B-1:

- 1. This step shows a series/parallel LC circuit designed from a low-pass filter prototype. All LC circuits resonate at  $\omega_0$ .
- 2. Admittance inverters  $J_1$  to  $J_{n+1}$  allow the series resonant circuits to be replaced by parallel resonant circuits. Two inverters surrounding a parallel LC circuit appear as a series LC circuit but change the impedance level as seen by the rest of the circuit. The inverters  $J_1$  and  $J_{n+1}$  act as transformers that change these levels to the desired levels.

The resonator slope parameters are  $b_1 \dots b_n$ . The slope parameters are defined by the following equation:

$$b_{K} = \frac{\omega_{0}}{2} \left( \frac{\partial B_{K}}{\partial \omega} \right)_{\omega = \omega_{0}}$$
 (B-1)

Note that the slope parameters for a simple LC parallel resonant circuit would be  $b_K = \omega_0 C_K$ . Quarter-wave shorted stubs that are used as resonators have slope parameters of  $y_{0_k} \cdot \frac{\pi}{4}$ . These act very similar to parallel LC resonant circuits since in both cases the admittance is zero at resonance and the change of admittance with frequency is the same.

The values of  $J_K$  and the end values  $J_1$  and  $J_{n+1}$  are based on the fractional bandwidth W, the resonator slope parameters, the low-pass prototype parameters, and the load admittances, as shown in figure B-1.

- 3. The capacitive  $\pi$  network is used with the ends throughout to simulate the inverters found in step 2. The series capacitances  $C_S$  are shunted on both sides by negative capacitances of the same value. The end inverter consists of a positive series capacitance and an unequal negative parallel capacitance.
- 4. The sum of negative capacitances are shown across each resonator. Note that the negative capacitances act like a positive inductance.
- 5. The admittance of a short-circuited coaxial line at 1/4 wavelength is zero. At frequencies slightly below this resonance the shorted stub acts like an inductance. Therefore, the stubs must be shorter than 1/4 wavelength at the center frequency. The coaxial line's characteristic admittance  $Y_0 = \frac{1}{Z_0}$  is determined by its capacitance per unit length. Since the required capacitance will be somewhat less than the original, a new value of  $Y_0'$  may be required. In practice, for narrow bandwidths this is really unnecessary. Figure B-2, step 5, shows the procedure necessary to use inductive coupling between resonators.

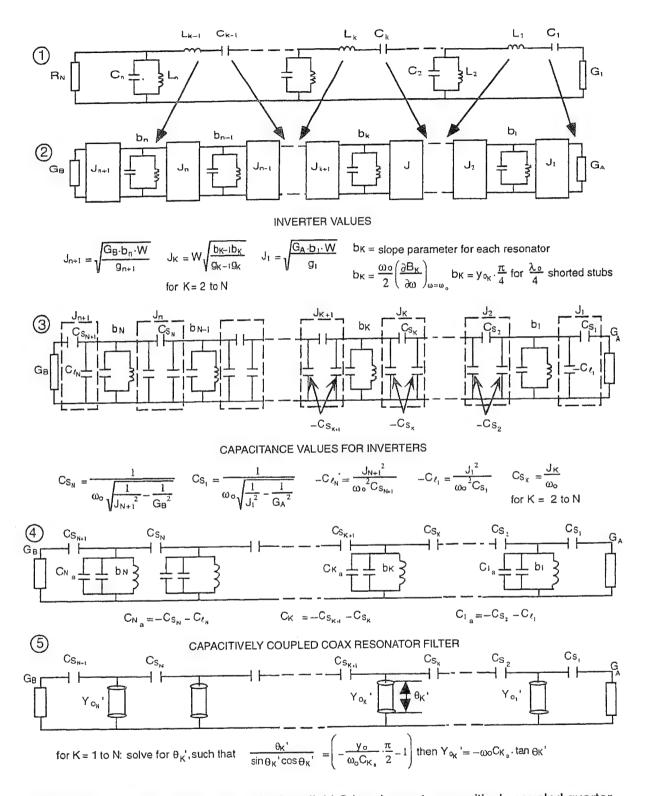


Figure B-1. Transformation from series/parallel LC band-pass to capacitively coupled quarterwave shorted lines.

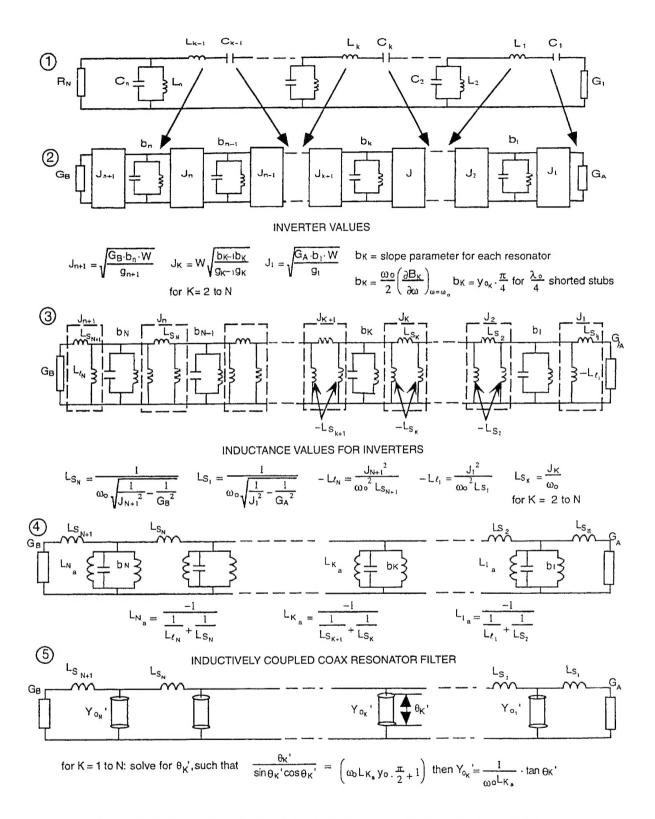


Figure B-2. Procedure for realizing inductive coupling between resonators.

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13. ABSTRACT (Maximum 200 words)

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The Multiplexers for Multifunction Electromagnetic Radiating Systems (MERS) objective was to merge four different radio frequency (RF) sensor systems into a single-antenna structure. Currently, the four candidates are Combat Direction Finding (CDF), Identification Friend or Foe (IFF), Joint Tactical Information Distribution System (JTIDS), and UHF communications. This report demonstrates that the merged antenna system is a low-cost system that decreases topside space and weight requirements while it also improves the multiple system performance. The ultimate goal is to provide a family of MERS antennas that meet each ship class antenna requirement in a combined and optimized structure. This report discusses the requirements, system concept for MERS, theoretical aspects, and the numerical results behind the multiplexer designs. Recommendations for future work are also included.

14. SUBJECT TERMS			15. NUMBER OF PAGES
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